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Form PTO-1449		U.S. Department of Commerce Patent And Trademark Office		ATTY. DOCKET NO. ITW 8637.62		SERIAL NO. 10/826534	
SUPPLEMENTAL INFORMATION DISCLOSURE CITATION				APPLICANT Steven J. Geissler			
				FILING DATE April 16, 2004		GROUP 2838	
U.S. PATENT DOCUMENTS							
EXAM. INIT	REF	PATENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
<input checked="" type="checkbox"/>		4,564,742	1/14/86	Karlsson			
<input checked="" type="checkbox"/>		4,683,529	7/28/87	Bucher, II			
<input checked="" type="checkbox"/>		5,086,205	2/4/92	Thommes			
<input checked="" type="checkbox"/>		5,149,935	9/22/92	Ozimek et al.			
<input checked="" type="checkbox"/>		5,444,356	8/22/95	Reynolds et al.			
<input checked="" type="checkbox"/>		5,563,777	10/8/96	Miki et al.			
<input checked="" type="checkbox"/>		6,002,103	12/14/99	Thommes			
<input checked="" type="checkbox"/>		6,239,407	5/29/01	Thommes			
<input checked="" type="checkbox"/>		6,815,639	11/6/04	Geissler			
<input checked="" type="checkbox"/>		6,849,827	2/1/05	Thommes			
FOREIGN PATENT DOCUMENTS							
EXAM INIT	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
<input checked="" type="checkbox"/>		DE4128175	93/02/25	Germany			
<input checked="" type="checkbox"/>		DE4211906	93/06/09	Germany			
<input checked="" type="checkbox"/>		DE4411227	95/10/05	Germany			
<input checked="" type="checkbox"/>		GB2258958	93/02/24	Great Britain			
OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc.)							
<input checked="" type="checkbox"/>		Unitrode Corporation, Boost Power Factor Corrector Design With The UC3853, By Philip C. Todd					
<input checked="" type="checkbox"/>		Design Issues For A Zero-Switched Power Factor Correction Circuit and DC/DC Converter Power Processing Unit, Y.V. Panov, J.A. Sabate, and F.C. Lee 1993 VPEC Reprinted with permission from <u>Proceedings of the Virginia Power Electronics Seminar, Blackburg, VA;</u> September 19-21, 1993; pp. 213-224					
EXAMINER C. SHAW				DATE CONSIDERED 8/8/2005			
EXAMINER: Initial if citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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EXAM INIT	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
✓		S63-190646	90-2-09	Japan			✓
✓		S63-42690	89-08-08	Japan			
✓		JP59220285	84-12-11	Japan			
✓		JP62107868	87-05-19	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc.)							
✓		Unitrode Corporation A High Performance Linear Regulator For Low Dropout Applications Dave Zendzian Applications Engineer					
✓		Unitrode Corporation, High Power Factor Switching Preregulator Design Optimization, Lloyd Dixon					
✓		Miller® Maxstar 200 STR Owners Manual October 2003					
✓		A Novel Single-Phase Power Factor Correction Scheme Y.M. Jiang, F.C. Lee, G.C. Hua, and W. Tang 1993 VPEC Reprinted with permission from <u>Proceedings of the Applied Power Electronics Conference</u> : San Diego, CA; March 7-11, 1993; pp. 287- 292					
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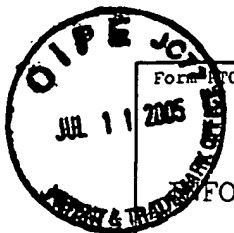
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OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc.)							
GA		Unitrode, Application Note, Power Factor Correction Using The UC3852 Controlled On-Time Zero Current Switching Technique, Bill Andreyckak					
GA		Reduction of Voltage Stress in Intrgrated High-Quality Rectifier-Regulators by Variable-Frequency Control, M.M. Jovanovic, D.M. Tsang, and F.C. Lee, 1994 VPEC. Reprinted with permission from <u>Proceedings of the Applied Power Electronics Conference</u> ; Orlando, FL; February 13-17, 1994; pp. 569-575.					
GA		Single-Stage Single-Phase Parallel Power Factor Correction Scheme, Y. Jiang and F.C. Lee 1994 VPEC. Reprinted with permission from <u>Proceedings of the Power Electronics Specialist Conference</u> ; Taipei, Taiwan; June 20-25, 1994					
GA		Miller ® <i>The Power of Blue</i> , Spectrum 2050, Owners Manual November 1999					
GA		Unitrode, Application Note, UC3854 Controlled Power Factor Correction Circuit Design, Philip C. Todd					
EXAMINER C. SHAW				DATE CONSIDERED 8/8/2005			
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